## SN5402, SN54LS02, SN54S02, SN7402, SN74LS02, SN74S02 QUADRUPLE 2-INPUT POSITIVE-NOR GATES

DECEMBER 1983-REVISED MARCH 1988

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

#### description

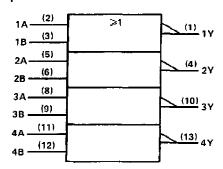
These devices contain four independent 2-input-NOR gates.

The SN5402, SN54LS02, and SN54S02 are characterized for operation over the full military temperature range of -55°C to 125°C. The SN7402, SN74LS02, and SN74S02 are characterized for operation from 0°C to 70°C.

#### **FUNCTION TABLE (each gate)**

	INP	UTS	OUTPUT
	A	В	Y
ſ	Н	X	L
1	Х	Н	L
Ì	L	L	J H J

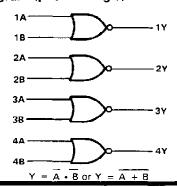
### logic symbol<sup>†</sup>



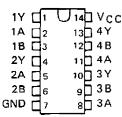
<sup>&</sup>lt;sup>†</sup>This symbol is in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, and N packages.

#### logic diagram (positive logic)



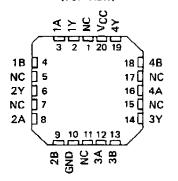
SN5402 . . . J PACKAGE
SN54LS02, SN54S02 . . . J OR W PACKAGE
SN7402 . . . N PACKAGE
SN74LS02, SN74S02 . . . D OR N PACKAGE
(TOP VIEW)



SN5402 . . . W PACKAGE (TOP VIEW)

1A 🗀	ſī	U 14	<b>∆</b> 4Y
18 🗀	2	13	_ 4B
1Y 🗀	3	12	□ 4A
Vcc □	4	- 11	B GND
2Y 🗀	5	10	] 3B
2A 🗀	6	9	] 3A
2B 🗀	7	8	] 3Y

SN54LS02, SN54S02 . . . FK PACKAGE (TOP VIEW)

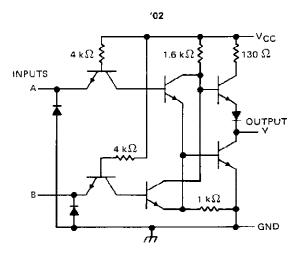


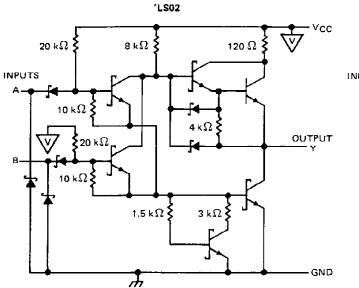
NC - No internal connection

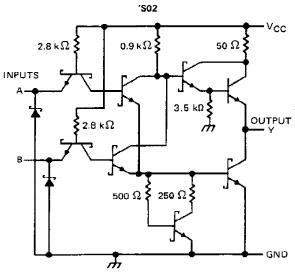
PRODUCTION DATA documents contain information current as of publication dats. Preducts conform to specifications per the terms of Tuxas Instruments standard warranty. Production processing does not necessarily include tasting of all parameters.



#### schematics (each gate)







Resistor values shown are nominal.

# absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1)	
Input voltage: '02, 'S02	
'LS02	
Off-state output voltage	, 7 V
Operating free-air temperature range:	SN54'
	SN74'
Storage temperature range	65°C to 150°C

NOTE 1. Voltage values are with respect to network ground terminal.



#### recommended operating conditions

	:	SN5402			SN7402			
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
V <sub>CC</sub> Supply voltage	4.5	5	5.5	4.75	5	5.25	ν	
V <sub>IH</sub> High-level input voltage	2			2			V	
VIL Low-level input voltage			8.0			0.8	V	
IOH High-level output current			- 0.4			- 0.4	mΔ	
IOL Low-level output current			16			16	mΑ	
TA Operating free-air temperature	55		125	٥		70	°c	

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

5450445755	7.5	TEST CONDITIONS †			SN5402			SN7402		
PARAMETER	( E	STCONDITIONST		MIN	TYP#	MAX	MIN	TYP‡	MAX	UNIT
Vικ	VCC = MIN, II =	— 12 mA				- 1.5			<b>–</b> 1. <b>5</b>	٧
Voн	VCC = MIN, VII	= 0.8 V, I <sub>OH</sub> = -	- 0.4 mA	2.4	3.4		2.4	3.4		٧
V <sub>OL</sub>	V <sub>CC</sub> = MIN, V <sub>II</sub>	1 = 2 V, IOL = 10	6 mA		0.2	0.4	,	0.2	0.4	٧
Ц	VCC = MAX, VI	= 5.5 V				1			1	mA
Ιн	VCC = MAX, VI	= 2.4 V	-			40			40	μΑ
h <sub>L</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub>	= 0.4 V				<b>- 1.6</b>			<b>- 1.6</b>	mΑ
IOS §	V <sub>CC</sub> = MAX			- 20		- 55	- 18		- 55	mA
<sup>1</sup> ССН	V <sub>CC</sub> = MAX, V <sub>I</sub>	- 0 V			8	16		8	16	mA
CCL	V <sub>CC</sub> = MAX, See	Note 2		ĺ	14	27		14	27	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 2: One input at 4.5 V, all others at GND.

# switching characteristics, $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t <sub>PLH</sub>	_				12	22	ns
<sup>t</sup> PHL	A or B	Υ	$R_L = 400 \Omega$ , $C_L = 15 pF$		8	15	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

<sup>‡</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ . § Not more than one output should be shorted at a time.

# SN54LS02, SN74LS02 QUADRUPLE 2-INPUT POSITIVE-NOR GATES

#### recommended operating conditions

	<del></del> -		SN54LS02			SN74LS02			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
V <sub>CC</sub> Supply volta	ge	4.5	5	5 <b>.5</b>	4.75	5	5.25	v	
VIH High-level inp	out voltage	2			2			٧	
VIL Low-level inc	out voltage			0.7		-	8.0	٧	
IOH High-level ou	tput current			- 0.4			- 0.4	mΑ	
IOL Low-level ou	tput current			4			8	mA	
T <sub>A</sub> Operating fre	e-air temporature	- 55		125	0		70	°C	

#### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

		TEST CONDITIONS †			SN54L	502		SN74L8	SO2	
PARAMETER				MIN	TYP‡	MAX	MIN	TYP\$	MAX	TINU
VIK	VCC = MIN,	I <sub>1</sub> = 18 mA				<b>— 1.5</b>			<b>– 1.5</b>	V
∨он	V <sub>CC</sub> = MIN,	VIL = MAX,	<sup>1</sup> OH = - 0.4 mA	2.5	3.4		2.7	3.4		٧
.,	V <sub>CC</sub> - MIN,	V <sub>1H</sub> = 2 V,	I <sub>OL</sub> = 4 mA		0.25	0.4		0.25	0.4	V
VOL	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V,	IOL = 8 mA					0.35	0.5	1 *
Ц	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 7 V	<b>.</b>			0.1			0.1	mΑ
Чн	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 2.7 V				20			20	μА
116	V <sub>CC</sub> = MAX,	V) = 0.4 V				- 0.4			- 0.4	mΑ
IOS§	V <sub>CC</sub> - MAX		· · · · · · · · · · · · · · · · · · ·	- 20		- 100	- 20		- 100	mΑ
ІССН	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 0 V			1.6	3.2		1.6	3.2	mΑ
<sup>1</sup> CCL	VCC = MAX,	See Note 2			2.8	5.4		2.8	5.4	mА

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

# switching characteristics, $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
<sup>₹</sup> PLH	A or B	B Υ R <sub>L</sub> = 2 kΩ	D 210			10	15	ns
tPHL	70.0		R <sub>L</sub> = 2 kΩ, C <sub>L</sub> = 15 pF			10	15	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

<sup>†</sup> All typical values are at  $V_{\rm CC}$  = 5 V,  $T_{\rm A}$  = 25°C § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second. NOTE 2: One input at 4.5 V, all others at GND.

#### recommended operating conditions

			SN54S02 SN74S02					
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	2		-	2			٧
٧١L	Low-level input voltage			8.0			0.8	٧
lон	High-level output current			- 1			<b>–</b> 1	mΑ
loL	Low-level output current			20			20	mΑ
Тд	Operating free-air temperature	55		125	0		70	°C

#### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED	TEST CONDITIONS †	SN54S02 SN74S02	
PARAMETER	TEST CONDITIONS	MIN TYP\$ MAX MIN TYP\$ MAX	דואט -
VIK	V <sub>CC</sub> = MIN, I <sub>I</sub> = -18 mA	-1.2 -1.3	2 V
V <sub>OH</sub>	$V_{CC}$ = MIN, $V_{IL}$ = 0.8 V, $I_{OH}$ = -1	nA 2.5 3.4 2.7 3.4	٧
VOL	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V, I <sub>OL</sub> = 20 I	A 0.5 0.6	V
Ц	V <sub>CC</sub> = MAX, V <sub>I</sub> = 5.5 V	1	mA
ЧН	V <sub>CC</sub> = MAX, V <sub>I</sub> = 2.7 V	50 50	) μΑ
կը	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0.5 V	-2 -2	! mA
l <sub>OS</sub> §	V <sub>CC</sub> = MAX	_40 _100 _40 _100	) mA
<sup>1</sup> ссн	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0 V	17 29 17 29	mA
CCL	V <sub>CC</sub> = MAX, See Note 2	26 45 26 45	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 2: One input at 4.5 V, all others at GND.

# switching characteristics, $V_{CC}$ = 5 V, $T_A$ = 25°C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN TYP	MAX	UNIT
tPLH		Y	$R_1 = 280 \Omega$ , $C_1 = 15 pF$	3.5	5,5	ns
tPHL			R <sub>L</sub> = 280 Ω, C <sub>L</sub> = 15 pF	3.5	5,5	ns
tPLH	A or B		$R_1 = 280 \Omega$ , $C_L = 50 pF$	5		ns
tPHL			$R_{\perp} = 280 \Omega$ , $C_{\perp} = 50  pF$	5		ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

<sup>‡</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_{\Delta} = 25^{\circ}\text{C}$ . § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.



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### **PACKAGING INFORMATION**

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead finish/ Ball material	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
JM38510/00401BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 00401BCA	Samples
JM38510/00401BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 00401BDA	Samples
JM38510/00401BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 00401BDA	Samples
JM38510/07301BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 07301BCA	Samples
JM38510/07301BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 07301BCA	Samples
JM38510/07301BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 07301BDA	Samples
JM38510/07301BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 07301BDA	Samples
JM38510/30301B2A	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301B2A	Samples
JM38510/30301B2A	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301B2A	Samples
JM38510/30301BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301BCA	Samples
JM38510/30301BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301BCA	Samples
JM38510/30301BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301BDA	Samples
JM38510/30301BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301BDA	Samples
JM38510/30301SDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301SDA	Samples
JM38510/30301SDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301SDA	Samples
M38510/00401BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 00401BCA	Samples





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Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead finish/ Ball material	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
M38510/00401BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 00401BCA	Samples
M38510/00401BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 00401BDA	Samples
M38510/00401BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 00401BDA	Samples
M38510/07301BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 07301BCA	Samples
M38510/07301BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 07301BCA	Samples
M38510/07301BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 07301BDA	Samples
M38510/07301BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 07301BDA	Samples
M38510/30301B2A	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301B2A	Samples
M38510/30301B2A	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301B2A	Samples
M38510/30301BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301BCA	Samples
M38510/30301BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301BCA	Samples
M38510/30301BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301BDA	Samples
M38510/30301BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301BDA	Samples
M38510/30301SDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301SDA	Samples
M38510/30301SDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 30301SDA	Samples
SN5402J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SN5402J	Samples
SN5402J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SN5402J	Samples





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Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead finish/ Ball material	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
SN54LS02J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SN54LS02J	Samples
SN54LS02J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SN54LS02J	Samples
SN54S02J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type -55 to 125		SN54S02J	Samples
SN54S02J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SN54S02J	Samples
SN7402N	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN7402N	Samples
SN7402N	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN7402N	Samples
SN74LS02D	ACTIVE	SOIC	D	14	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS02	Samples
SN74LS02D	ACTIVE	SOIC	D	14	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS02	Samples
SN74LS02DE4	ACTIVE	SOIC	D	14	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS02	Samples
SN74LS02DE4	ACTIVE	SOIC	D	14	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS02	Samples
SN74LS02DG4	ACTIVE	SOIC	D	14	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS02	Samples
SN74LS02DG4	ACTIVE	SOIC	D	14	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS02	Samples
SN74LS02DR	ACTIVE	SOIC	D	14	2500	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS02	Samples
SN74LS02DR	ACTIVE	SOIC	D	14	2500	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS02	Samples
SN74LS02DRE4	ACTIVE	SOIC	D	14	2500	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS02	Samples
SN74LS02DRE4	ACTIVE	SOIC	D	14	2500	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS02	Samples
SN74LS02N	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN74LS02N	Samples
SN74LS02N	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN74LS02N	Samples
SN74LS02NE4	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN74LS02N	Samples
SN74LS02NE4	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN74LS02N	Samples





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Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead finish/ Ball material	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
SN74LS02NSR	ACTIVE	so	NS	14	2000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS02	Samples
SN74LS02NSR	ACTIVE	SO	NS	14	2000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS02	Samples
SN74LS02NSRG4	ACTIVE	SO	NS	14	2000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS02	Samples
SN74LS02NSRG4	ACTIVE	SO	NS	14	2000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS02	Samples
SN74S02D	ACTIVE	SOIC	D	14	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	S02	Samples
SN74S02D	ACTIVE	SOIC	D	14	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	S02	Samples
SN74S02N	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN74S02N	Samples
SN74S02N	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN74S02N	Samples
SNJ5402J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ5402J	Samples
SNJ5402J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ5402J	Samples
SNJ5402W	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ5402W	Samples
SNJ5402W	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ5402W	Samples
SNJ54LS02FK	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54LS 02FK	Samples
SNJ54LS02FK	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54LS 02FK	Samples
SNJ54LS02J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54LS02J	Samples
SNJ54LS02J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54LS02J	Samples
SNJ54LS02W	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54LS02W	Samples
SNJ54LS02W	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54LS02W	Samples
SNJ54S02FK	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54S 02FK	Samples



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Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead finish/ Ball material	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
SNJ54S02FK	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54S 02FK	Samples
SNJ54S02J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54S02J	Samples
SNJ54S02J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54S02J	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (CI) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead finish/Ball material Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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# **PACKAGE OPTION ADDENDUM**

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#### OTHER QUALIFIED VERSIONS OF SN5402, SN54LS02, SN54LS02-SP, SN54S02, SN7402, SN74LS02, SN74S02:

• Catalog: SN7402, SN74LS02, SN54LS02, SN74S02

• Military : SN5402, SN54LS02, SN54S02

• Space : SN54LS02-SP

NOTE: Qualified Version Definitions:

- Catalog TI's standard catalog product
- Military QML certified for Military and Defense Applications
- Space Radiation tolerant, ceramic packaging and qualified for use in Space-based application

# **PACKAGE MATERIALS INFORMATION**

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#### TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

#### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



#### \*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74LS02DR	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1
SN74LS02NSR	so	NS	14	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1

**PACKAGE MATERIALS INFORMATION** 

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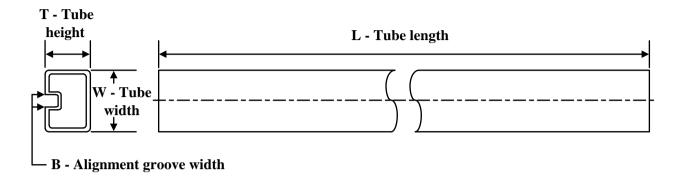
#### \*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74LS02DR	SOIC	D	14	2500	356.0	356.0	35.0
SN74LS02NSR	so	NS	14	2000	356.0	356.0	35.0



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#### **TUBE**



#### \*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (µm)	B (mm)
JM38510/00401BDA	W	CFP	14	1	506.98	26.16	6220	NA
JM38510/07301BDA	W	CFP	14	1	506.98	26.16	6220	NA
JM38510/30301B2A	FK	LCCC	20	1	506.98	12.06	2030	NA
JM38510/30301BDA	W	CFP	14	1	506.98	26.16	6220	NA
JM38510/30301SDA	W	CFP	14	1	506.98	26.16	6220	NA
M38510/00401BDA	W	CFP	14	1	506.98	26.16	6220	NA
M38510/07301BDA	W	CFP	14	1	506.98	26.16	6220	NA
M38510/30301B2A	FK	LCCC	20	1	506.98	12.06	2030	NA
M38510/30301BDA	W	CFP	14	1	506.98	26.16	6220	NA
M38510/30301SDA	W	CFP	14	1	506.98	26.16	6220	NA
SN7402N	N	PDIP	14	25	506	13.97	11230	4.32
SN7402N	N	PDIP	14	25	506	13.97	11230	4.32
SN74LS02D	D	SOIC	14	50	506.6	8	3940	4.32
SN74LS02DE4	D	SOIC	14	50	506.6	8	3940	4.32
SN74LS02DG4	D	SOIC	14	50	506.6	8	3940	4.32
SN74LS02N	N	PDIP	14	25	506	13.97	11230	4.32
SN74LS02N	N	PDIP	14	25	506	13.97	11230	4.32
SN74LS02NE4	N	PDIP	14	25	506	13.97	11230	4.32
SN74LS02NE4	N	PDIP	14	25	506	13.97	11230	4.32
SN74S02D	D	SOIC	14	50	506.6	8	3940	4.32
SN74S02N	N	PDIP	14	25	506	13.97	11230	4.32
SN74S02N	N	PDIP	14	25	506	13.97	11230	4.32
SNJ5402W	W	CFP	14	1	506.98	26.16	6220	NA
SNJ54LS02FK	FK	LCCC	20	1	506.98	12.06	2030	NA
SNJ54LS02W	W	CFP	14	1	506.98	26.16	6220	NA
SNJ54S02FK	FK	LCCC	20	1	506.98	12.06	2030	NA

### **MECHANICAL DATA**

# NS (R-PDSO-G\*\*)

# 14-PINS SHOWN

#### PLASTIC SMALL-OUTLINE PACKAGE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



# W (R-GDFP-F14)

# CERAMIC DUAL FLATPACK



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only.
- E. Falls within MIL STD 1835 GDFP1-F14



8.89 x 8.89, 1.27 mm pitch

LEADLESS CERAMIC CHIP CARRIER

This image is a representation of the package family, actual package may vary. Refer to the product data sheet for package details.



CERAMIC DUAL IN LINE PACKAGE



Images above are just a representation of the package family, actual package may vary. Refer to the product data sheet for package details.

4040083-5/G





CERAMIC DUAL IN LINE PACKAGE



- 1. All controlling linear dimensions are in inches. Dimensions in brackets are in millimeters. Any dimension in brackets or parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
- 2. This drawing is subject to change without notice.
- 3. This package is hermitically sealed with a ceramic lid using glass frit.
- His package is remitted by sealed with a ceramic its using glass mit.
   Index point is provided on cap for terminal identification only and on press ceramic glass frit seal only.
   Falls within MIL-STD-1835 and GDIP1-T14.



CERAMIC DUAL IN LINE PACKAGE



# D (R-PDSO-G14)

### PLASTIC SMALL OUTLINE



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AB.



# D (R-PDSO-G14)

# PLASTIC SMALL OUTLINE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Publication IPC-7351 is recommended for alternate designs.
- D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
- E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.



# N (R-PDIP-T\*\*)

# PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- The 20 pin end lead shoulder width is a vendor option, either half or full width.



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